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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Mahajani et al.

Application No.: 10/668,693

Group Art Unit: 2826

Filed: 9/23/03

Examiner: Victor A. Mandala

Title: Storage Layer Optimization of a  
Nonvolatile Memory Device

Attorney Docket No.: MA-105

Commissioner for Patents  
PO Box 1450  
Alexandria VA 22313-1450

July 13, 2005

**AMENDMENT AND RESPONSE TO NON-FINAL OFFICE ACTION**

To the Commissioner:

This paper is filed in response to the non-final office action mailed March 14, 2005, in the above-captioned case. A complete listing of currently pending claims begins on page 2. A discussion of claim amendments begins on page 10. Remarks begin on page 13, and a Conclusion appears on page 19. A replacement drawing sheet for drawing sheet 3 is included with this response.